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|----|------|-------------|--|--|---------------------|
| 1  | BRS  | 583824      | (add\$4 or modif\$4 or remov\$4 or updat\$4 or replac\$4) near5 (equipment or controller or system)  | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:23 |
| 2  | BRS  | 596844      | (add\$4 or modif\$4 or remov\$4 or updat\$4 or replac\$6) near5 (equipment or controller or system)  | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:24 |
| 3  | BRS  | 65279       | address\$2 with table\$2   | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:26 |
| 4  | BRS  | 583824      | ((add\$4 or modif\$4 or remov\$4 or updat\$4 or replac\$4) near5 (equipment or controller or system) ) same ((add\$4 or modif\$4 or remov\$4 or updat\$4 or replac\$6) near5 (equipment or controller or system) ) | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:26 |
| 5  | BRS  | 180433<br>5 | table or matrix or tabulat\$4  | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:27 |
| 6  | BRS  | 33173       | ((add\$4 or modif\$4 or remov\$4 or updat\$4 or replac\$4) near5 (equipment or controller or system) ) same (table or matrix or tabulat\$4)  | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:27 |
| 7  | BRS  | 7952        | (address\$2 with table\$2) same (((add\$4 or modif\$4 or remov\$4 or updat\$4 or replac\$4) near5 (equipment or controller or system) ) same (table or matrix or tabulat\$4))                                      | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:27 |
| 8  | BRS  | 7952        | (address\$2 with table\$2) and ((address\$2 with table\$2) same (((add\$4 or modif\$4 or remov\$4 or updat\$4 or replac\$4) near5 (equipment or controller or system) ) same (table or matrix or tabulat\$4)))     | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:28 |
| 9  | BRS  | 84990       | second near10 table  | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:28 |
| 10 | BRS  | 113007      | first near10 table   | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:29 |
| 11 | BRS  | 8849        | corresponden\$4 near10 address\$4  | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:29 |

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| 12 | BRS  | 228    | ((address\$2 with table\$2) and ((address\$2 with table\$2) same (((add\$4 or modif\$4 or remov\$4 or updat\$4 or replac\$4) near5 (equipment or controller or system) ) same (table or matrix or tabulat\$4)))) and (second near10 table) and (first near10 table) and (corresponde\$4 near10 address\$4)   | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:29 |
| 13 | BRS  | 17253  | third near5 table  | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:30 |
| 14 | BRS  | 72     | ((address\$2 with table\$2) and ((address\$2 with table\$2) same (((add\$4 or modif\$4 or remov\$4 or updat\$4 or replac\$4) near5 (equipment or controller or system) ) same (table or matrix or tabulat\$4)))) and (second near10 table) and (first near10 table) and (corresponde\$4 near10 address\$4)) and (third near5 table)                    | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:30 |
| 15 | BRS  | 152083 | new and old  | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:30 |
| 16 | BRS  | 19     | ((address\$2 with table\$2) and ((address\$2 with table\$2) same (((add\$4 or modif\$4 or remov\$4 or updat\$4 or replac\$4) near5 (equipment or controller or system) ) same (table or matrix or tabulat\$4)))) and (second near10 table) and (first near10 table) and (corresponde\$4 near10 address\$4)) and (third near5 table)) and (new and old) | USPAT;<br>US-PGPUB; EPO;<br>JPO; DERWENT;<br>IBM_TDB | 2004/05/25<br>12:30 |

|    | Document ID        | Issue Date | Page s | Title   | Current OR | Current XRef  | Inventor                   |
|----|--------------------|------------|--------|---|------------|---|----------------------------|
| 1  | US 2003017971 2 A1 | 20030925   | 1116   | CONNECTIONLESS COMMUNICATIONS SYSTEM, ITS TEST METHOD, AND INTRA-STATION CONTROL SYSTEM                     | 370/249    | 370/250; 370/368; 710/24                                | KOBAYASHI, YASUHI et al.   |
| 2  | US 2002019311 6 A1 | 20021219   | 31     | Network-layer and link-layer use of shadow addresses with IP-based base stations                            | 455/445    |   | Agrawal, Prathima et al.   |
| 3  | US 2002019311 4 A1 | 20021219   | 32     | Network-layer and link-layer use of shadow addresses in soft handoff within subnets                         | 455/442    | 370/335   | Agrawal, Prathima et al.   |
| 4  | US 2002019156 1 A1 | 20021219   | 32     | Packet distribution and selection in soft handoff for IP-based base stations among multiple subnets         | 370/331    | 370/338; 370/349; 370/352; 370/401; 455/442             | Chen, Jyh-Cheng et al.     |
| 5  | US 2002019156 0 A1 | 20021219   | 31     | Distributed smooth handoff using shadow addresses in IP-based base stations                                 | 370/331    | 370/338; 370/349; 370/352; 370/401; 455/436             | Chen, Jyh-Cheng et al.     |
| 6  | US 2002019155 8 A1 | 20021219   | 32     | Distributed soft handoff among IP-based base stations   | 370/329    | 370/473   | Agrawal, Prathima et al.   |
| 7  | US 2002005139 4 A1 | 20020502   | 139    | Flash memory control method and apparatus processing system therewith                                       | 365/221    |   | Tobita, Tsunehiro et al.   |
| 8  | US 6715057 B1      | 20040330   | 17     | Efficient translation lookaside buffer miss processing in computer systems with a large range of page sizes | 711/207    |   | Kessler, Richard E. et al. |
| 9  | US 6421279 B1      | 20020716   | 137    | Flash memory control method and apparatus processing system therewith                                       | 365/189.01 | 365/189.04; 365/189.05; 365/233                         | Tobita, Tsunehiro et al.   |
| 10 | US 6275436 B1      | 20010814   | 136    | Flash memory control method and apparatus processing system therewith                                       | 365/221    | 365/189.01; 365/189.05; 365/233; 365/49                 | Tobita, Tsunehiro et al.   |
| 11 | US 6078520 A       | 20000620   | 136    | Flash memory control method and information processing system therewith                                     | 365/185.09 | 365/185.11; 365/185.29; 365/185.33; 365/200; 365/230.03 | Tobita, Tsunehiro et al.   |

|    | Document ID  | Issue Date | Page s | Title  | Current OR | Current XRef   | Inventor                 |
|----|--------------|------------|--------|--|------------|--|--------------------------|
| 12 | US 6049825 A | 20000411   | 17     | Method and system for switching between duplicated network interface adapters for host computer communications | 709/221    | 709/220; 714/2   | Yamamoto, Shinji         |
| 13 | US 5973964 A | 19991026   | 136    | Flash memory control method and information processing system therewith  | 365/185.29 | 365/185.11; 365/189.01; 365/218  | Tobita, Tsunehiro et al. |
| 14 | US 5963672 A | 19991005   | 79     | Data encoding and decoding systems   | 382/238    | 348/394.1; 358/539; 382/245; 382/247   | Yajima, Akihiko et al.   |
| 15 | US 5940597 A | 19990817   | 23     | Method and apparatus for periodically updating entries in a content addressable memory                         | 709/242    | 711/149  | Chung, David H.          |
| 16 | US 5862083 A | 19990119   | 136    | Information processing system  | 365/185.09 | 365/185.11; 365/185.22; 365/189.07; 365/230.03   | Tobita, Tsunehiro et al. |
| 17 | US 5764804 A | 19980609   | 83     | Data encoding and decoding system  | 382/238    | 348/27; 348/38; 348/642; 358/426.02; 358/500; 358/539; 359/563; 380/54; 382/131; 382/245; 382/247; 409/165 | Yajima, Akihiko et al.   |
| 18 | US 5530673 A | 19960625   | 136    | Flash memory control method and information processing system therewith  | 365/185.09 | 365/185.04; 365/185.11; 365/185.22; 365/185.33; 365/201; 714/710; 714/718                                  | Tobita, Tsunehiro et al. |
| 19 | US 5428758 A | 19950627   | 14     | Method and system for remapping memory from one physical configuration to another physical configuration       | 711/165    | 711/202  | Salsburg, Linda B.       |

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| Set | Items  | Description  |
|-----|--------|--|
| S1  | 706366 | (REPLAC? OR MODIF? OR SWITCH?) (10N) (EQUIPMENT OR SYSTEM) |
| S2  | 109537 | NEW (10N) PROCESSOR  |
| S3  | 571056 | NEW (10N) EQUIPMENT  |
| S4  | 119    | S1 (S) S2 (S) S3   |
| S5  | 169    | TABLE (10N) (CORRESPOND? (10N) ADDRESS?)                   |
| S6  | 0      | S4 AND S5  |
| S7  | 0      | S4 AND S5  |
| S8  | 18797  | TABLE? (10N) ADDRESS?                                      |
| S9  | 154    | S5 AND S8  |
| S10 | 0      | S4 AND S9  |
| S11 | 19196  | CORRESPOND? (10N) ADDRESS?                                 |
| S12 | 169    | S5 AND S11   |
| S13 | 0      | S4 AND S11   |

|     |     |             |
|-----|-----|-------------|
| S14 | 154 | S5 AND S8   |
| S15 | 169 | S5 AND S11  |
| S16 | 154 | S14 AND S15 |
| S17 | 0   | S4 AND S11  |
| S18 | 1   | S4 AND S8   |
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00935320 20030217PHM006 (THIS IS THE FULLTEXT)  
Agere Systems Announces World's Fastest Network Processor  
PR Newswire  
Monday, February 17, 2003 00:02 EST  
JOURNAL CODE: PR LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT  
DOCUMENT TYPE: NEWSWIRE  
WORD COUNT: 1,535

TEXT:

ALLENTOWN, Pa., Feb. 17 /PRNewswire-FirstCall/ - Agere Systems (NYSE: AGR.A, AGR.B) today announced the world's fastest network processor that integrates four separate chips into one. This next generation network processor, called the PayloadPlus(R) APP540, makes possible revolutionary improvements in product development costs, service revenue capabilities, and reliability of communications network equipment for the next several years.

Agere's new chip can reduce product development costs -- a critical metric in the current cash-constrained market -- by at least 50 percent compared with the nearest contending chips.

Korea-based Electronics and Telecommunications Research Institute (ETRI), one of the world's leading information technology research and development organizations, has selected Agere's new APP540 network processor as the key engine for creating Korea Telecom's country-wide next generation network. ETRI, which has a long-standing relationship with Agere, has been instrumental in enabling Korea to achieve the highest percentage of broadband connections to citizens of any country in the world.

Agere's key technical breakthrough centers on integrating four separate devices -- programmable traffic manager, multi-field classifier search engine, network processor, and Ethernet media access controller (MAC) -- into a single device capable of processing information at speeds of 5 gigabits per second (Gbits/s). Competing products require at least two chips, and in several cases three or more, to perform these four different functions at comparable speeds.

Using fewer chips decreases electronics costs, power consumption, and equipment size, as well as increases reliability.

Classification, traffic management, and network processing function like three different traffic police officers working in unison with separate yet inter-dependent functions. The classifier determines what should be done with the voice, data, or video information entering communications equipment. The

network processor obeys processing and forwarding directions the classifier gives it. Robust traffic management helps control the flow of information exiting the network processor to most efficiently use the available bandwidth.

Robust traffic management, which supports true bandwidth and delay guarantees to 8,192 queues or more of individually scheduled streams of traffic, is important to support high-value service-level agreements for telecom carriers.

The Ethernet MAC allows direct connection to Ethernet, the world's most

popular form of data communications.

Roughly one-fourth the size of a credit card, Agere's APP540 chip is part of Agere's growing family of integrated Payload Plus network processors, traffic management and system software solutions used in communications equipment platforms. The PayloadPlus family has been selected for use in more than 70 system designs, more than half of which are top-tier manufacturers of communications equipment. In the multi-service equipment chip business, Agere possesses greater than 50 percent market share of the top-tier customers.

"Now more than ever, the communications equipment and service markets care about reducing capital and operations costs, rapidly and easily deploying new revenue generating services, and offering quick, simple, and flexible access to services over current and future networks," said Linley Gwennap, principal analyst with The Linley Group, a California-based semiconductor technology research company. "Agere's new highly integrated solution offers substantial cost reduction and programmability benefits that will maximize the value service providers can extract from their network investments.

"Agere is a leader in traffic management, having developed several generations of successful products," Gwennap added. "This technology puts the company in a strong position as traffic management becomes integrated into the network processor."

According to Ryan, Hankin, and Kent, a market research company, Agere ranks first in the world in sales of traffic management chips.\*\* "Building chips that do network processing but not traffic management is like building a road system without traffic signals such as stoplights and yield signs," said John Rolfe, marketing manager with Agere Systems.

"Recognizing the challenging environment in the communications market, Agere has been investing in advanced system chips targeted at platforms that can be consolidated and re-used across various equipment," said Mark Pinto, vice president of Agere's network processor business. "Our customers keep coming back to us with the same messages: cost reduction, multi-service revenue generation, and better reliability -- all provided in fewer and more flexible platforms. That's what this new integrated chip is all about."

Agere's PayloadPlus chips are used in various types of corporate office building and telecommunications central office equipment. Such equipment includes multi-service provisioning platforms and switches, routers, data center switches, 2.5-generation and third-generation wireless equipment

, Ethernet over Synchronous Optical Network (SONET) /Synchronous Digital Hierarchy (SDH) add/drop multiplexers, and SONET transmission systems.

"There is some ambiguity in the industry around what exactly is a network processor," said Agere's Pinto. "Some think it's a general purpose micro processor with networking interfaces and large data buffers. Some call it a look-up engine. Others a traffic manager. Still others consider it a segmentation and reassembly controller engine. Agere Systems believes network processors require the interworking of all of the above, as embodied in the single chip APP540.

"As an alternative, the industry could define the collection of chips, that perform traffic management, classification, and other network



processor functions, required in a given system as traffic processors," Pinto added. "Because that's what the chips do, process traffic. The APP500 family offers the full range of these functions in single chips, thereby lowering development costs, power and size of equipment."

Agere's APP540 chip uses external dynamic random access memory (DRAM) chips to house classification tables and rules. Competitors use content addressable memory (CAM) or static random access memory (SRAM) chips. In a cost per information bit comparison, CAMs are more than 100 times more expensive and consume more power than DRAMs. Agere didn't just integrate four devices and memory on a single chip. Rather, it developed an architecture that takes advantage of all the various functions and inter-dependencies of those functions onto a single device.

As part of Agere's broad portfolio of network processor-based solutions, Agere also provides a high-level, application-oriented software programming environment. This environment, which can reduce both the complexity and size of software code required by a factor of 25 or more, is included in Agere's Festino(TM) comprehensive hardware and software development platform. Equipment makers using Festino can deliver their product to market several months faster and accomplish in days what would normally take them months using alternative technologies. Furthermore, the reduction in software complexity and size can save millions of dollars in software-related costs over the life of the equipment.

Agere Systems is also announcing today the PayloadPlus APP520 chip, which is essentially the same device as the APP540 yet targeted at lower-cost applications. Agere's two new chips are completely designed and ready for manufacturing now. They are scheduled to start sampling to customers in April. In quantities of 10,000, the chips are priced at \$295 and \$195 respectively.

For more product information, customers may call the Agere Systems Customer Response Center, 1-800-372-2447, Dept. B02 (in Canada, 1-800-553-2448, Dept. B02, fax number 1-610-712-4106, especially for callers outside of North America) or write to Agere Systems, Room 10A-301C, 1110 American Parkway NE, Lehigh Valley Central Campus, Allentown, Pa. 18109. Customers may also go to the following web site: <http://www.agere.com/micro/his> or email: [docmaster@agere.com](mailto:docmaster@agere.com).

Agere Systems is a premier provider of advanced integrated circuit solutions that access, move and store network information. Agere's access portfolio enables seamless network access and Internet connectivity through its industry-leading WiFi/802.11 solutions for wireless LANs and computing applications, as well as its GPRS offering for data-capable cellular phones.

The company also provides custom and standard multi-service networking solutions, such as broadband Ethernet-over-SONET/SDH components and wireless infrastructure chips, to move information across metro, access and enterprise networks. Agere is the market leader in providing integrated circuits such as read-channel chips, preamplifiers and system-on-a-chip solutions for high-density storage applications. Agere's customers include the leading PC manufacturers, wireless terminal providers, network equipment suppliers and hard-disk drive providers. More information about Agere Systems is available

from its Web site at <http://www.agere.com>.

**Agere's Forward-Looking Statements**

This release contains forward-looking statements based on information available to Agere as of the date hereof. Agere's actual results could differ materially from the results stated or implied by such forward-looking statements due to a number of risks and uncertainties. These risks and uncertainties include, but are not limited to, keeping pace with technological change, dependence on new product development, price and product competition, availability of manufacturing capacity, customer demand for our products and services, general industry and market conditions, timely completion of employment reductions and other restructuring and consolidation activities, limits on our ability to issue equity to raise capital and reliance on major customers and suppliers. For a further discussion of these and other risks and uncertainties, see our annual report on Form 10-K for the fiscal year ended September 30, 2002. Agere disclaims any intention or obligation to update or revise any forward-looking statements, whether as a result of new information, future events or otherwise.

\* ETRI

\*\* Ryan, Hankin, and Kent

**SOURCE Agere Systems**

CONTACT: Charlie Hartley, +1-610-712-1728, +1-908-507-6631 (cellular), [cjhartley@agere.com](mailto:cjhartley@agere.com); or Steve Goldsmith, +1-610-712-6737, +1-484-357-0216, (cellular), [goldsmith@agere.com](mailto:goldsmith@agere.com), both of Agere Systems  
Web site: <http://www.agere.com/micro/his>  
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EVENT NAMES: PRODUCT LAUNCHES; NEW PRODUCT DEVELOPMENT; ADVERTISING AND PROMOTION; CORPORATE FINANCIAL DATA; MANUFACTURING AND PRODUCTION; MARKET RESEARCH; PRODUCT APPLICATIONS; PRODUCTIVITY; RESEARCH AND DEVELOPMENT; TECHNOLOGY DEVELOPMENT

**TEXT:**

...possible revolutionary improvements in product development costs, service revenue capabilities, and reliability of communications network equipment for the next several years.  
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| S8  | 18797  | TABLE? (10N) ADDRESS?                                      |
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| S10 | 0      | S4 AND S9  |
| S11 | 19196  | CORRESPOND? (10N) ADDRESS?                                 |
| S12 | 169    | S5 AND S11   |
| S13 | 0      | S4 AND S11   |
| S14 | 154    | S5 AND S8  |
| S15 | 169    | S5 AND S11   |
| S16 | 154    | S14 AND S15  |
| S17 | 0      | S4 AND S11   |
| S18 | 1      | S4 AND S8  |

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276573 BUILDING

19419 WAREHOUSE

178256 HOUSE

192736 HOME

132937 FACILITY

1242213 COMPUTER

35554 (((BUILDING OR WAREHOUSE) OR HOUSE) OR HOME) OR FACILITY) (S) COMPUTER

1242213 COMPUTER

1722496 CONTROL????

145576 COMPUTER(S) CONTROL????

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202194 REPLAC????

10834 COMPUTER(S) REPLAC????

S2 166 S1 AND COMPUTER (S) REPLAC????

?s s2 not py>2000

>>>One or more prefixes are unsupported

>>> or undefined in one or more files.

166 S2

1723891 PY>2000

S3 158 S2 NOT PY>2000

?RD

...examined 50 records (50)

...examined 50 records (100)

...examined 50 records (150)

...completed examining records

S4 152 RD (unique items)

SYSTEM:OS - DIALOG OneSearch

File 15:ABI/Inform(R) 1971-2002/Sep 24

(c) 2002 ProQuest Info&Learning

**\*File 15: Alert feature enhanced for multiple files, duplicate removal, customized scheduling. See HELP ALERT.**

File 16:Gale Group PROMT(R) 1990-2002/Sep 25

(c) 2002 The Gale Group

**\*File 16: Alert feature enhanced for multiple files, duplicate removal, customized scheduling. See HELP ALERT.**

File 148:Gale Group Trade & Industry DB 1976-2002/Sep 25

(c)2002 The Gale Group

**\*File 148: Alert feature enhanced for multiple files, duplicate removal, customized scheduling. See HELP ALERT.**

File 160:Gale Group PROMT(R) 1972-1989

(c) 1999 The Gale Group

File 275:Gale Group Computer DB(TM) 1983-2002/Sep 25

(c) 2002 The Gale Group

File 621:Gale Group New Prod.Annou.(R) 1985-2002/Sep 24

(c) 2002 The Gale Group

File 9:Business & Industry(R) Jul/1994-2002/Sep 24

(c) 2002 Resp. DB Svcs.

File 20:Dialog Global Reporter 1997-2002/Sep 25

(c) 2002 The Dialog Corp.

File 476:Financial Times Fulltext 1982-2002/Sep 25

(c) 2002 Financial Times Ltd

File 610:Business Wire 1999-2002/Sep 25

(c) 2002 Business Wire.

**\*File 610: File 610 now contains data from 3/99 forward.**

Archive data (1986-2/99) is available in File 810.

File 613:PR Newswire 1999-2002/Sep 25

(c) 2002 PR Newswire Association Inc

**\*File 613: File 613 now contains data from 5/99 forward.**

Archive data (1987-4/99) is available in File 813.

File 624:McGraw-Hill Publications 1985-2002/Sep 24

(c) 2002 McGraw-Hill Co. Inc

File 634:San Jose Mercury Jun 1985-2002/Sep 24

(c) 2002 San Jose Mercury News

File 636:Gale Group Newsletter DB(TM) 1987-2002/Sep 25

(c) 2002 The Gale Group

File 810:Business Wire 1986-1999/Feb 28

(c) 1999 Business Wire

File 813:PR Newswire 1987-1999/Apr 30

(c) 1999 PR Newswire Association Inc

Set Items Description

--- -----

?s (building or facility or house) and terminal (5n) replac?????

Processed 10 of 16 files ...

Processing

Completed processing all files

4116990 BUILDING

2289817 FACILITY

3320319 HOUSE

626850 TERMINAL

2624586 REPLAC?????

5558 TERMINAL(5N)REPLAC?????

S1 2177 (BUILDING OR FACILITY OR HOUSE) AND TERMINAL (5N)  
REPLAC?????

?s s1 and point (2n) address

2177 S1

4116544 POINT

2282187 ADDRESS

3072 POINT(2N)ADDRESS

S2 0 S1 AND POINT (2N) ADDRESS

?s s1 and control???? (3n) (replac????? or switch???)

Processing

Processed 10 of 16 files ...

Completed processing all files

```
      2177 S1
      6952275 CONTROL????
      2624586 REPLAC?????
      1675071 SWITCH???
      66962 CONTROL????(3N) (REPLAC????? OR SWITCH???)
S3      156 S1 AND CONTROL???? (3N) (REPLAC????? OR SWITCH???)
?s s3 and computer
      156 S3
      6669187 COMPUTER
S4      84 S3 AND COMPUTER
?s s4 not py>2000
      84 S4
      14465632 PY>2000
S5      75 S4 NOT PY>2000
?RD
...examined 50 records (50)
...completed examining records
S6      56 RD (unique items)
```

?s monitor??? (5n) replac???? and monitor??? (s) (house or building or facility)  
Processing  
Processed 10 of 16 files ...  
Completed processing all files  
2196067 MONITOR???  
2624586 REPLAC?????  
6600 MONITOR??? (5N) REPLAC?????  
2196067 MONITOR???  
3320319 HOUSE  
4116990 BUILDING  
2289817 FACILITY  
98606 MONITOR??? (S) ((HOUSE OR BUILDING) OR FACILITY)  
S7 504 MONITOR??? (5N) REPLAC????? AND MONITOR??? (S) (HOUSE OR  
BUILDING OR FACILITY)  
?s s7 and computer (s) monitor???  
Processed 10 of 16 files ...  
Processing  
Completed processing all files  
504 S7  
6669187 COMPUTER  
2196067 MONITOR???  
177381 COMPUTER(S) MONITOR???  
S8 194 S7 AND COMPUTER (S) MONITOR???  
?s s8 and replac????? (s) (equipment or terminal)  
Processed 10 of 16 files ...  
Processing  
Completed processing all files  
194 S8  
2624586 REPLAC?????  
7652559 EQUIPMENT  
626850 TERMINAL  
134074 REPLAC????? (S) (EQUIPMENT OR TERMINAL)  
S9 55 S8 AND REPLAC????? (S) (EQUIPMENT OR TERMINAL)  
?s s9 not py>2000  
55 S9  
14465632 PY>2000  
S10 49 S9 NOT PY>2000  
?RD  
...completed examining records  
S11 41 RD (unique items)



?s control???? (5n) replac???? and replac???? (5n) (terminal or equipment)

Processing

Processed 10 of 16 files ...

Processing

Completed processing all files

6952275 CONTROL????

2624586 REPLAC????

28067 CONTROL???? (5N) REPLAC????

2624586 REPLAC????

626850 TERMINAL

7652559 EQUIPMENT

56311 REPLAC???? (5N) (TERMINAL OR EQUIPMENT)

S12 1761 CONTROL???? (5N) REPLAC???? AND REPLAC???? (5N)  
(TERMINAL OR EQUIPMENT)

?s s12 and (building or house or home or facility) (5n) (management or control???? or replac????)

Processing

Processing

Processing

Processed 10 of 16 files ...

Processing

Completed processing all files

1761 S12

4116990 BUILDING

3320319 HOUSE

5841800 HOME

2289817 FACILITY

10921530 MANAGEMENT

6952275 CONTROL????

2624586 REPLAC????

383152 (((BUILDING OR HOUSE) OR HOME) OR  
FACILITY) (5N) ((MANAGEMENT OR CONTROL????) OR REPLAC????)

S13 357 S12 AND (BUILDING OR HOUSE OR HOME OR FACILITY) (5N)  
(MANAGEMENT OR CONTROL???? OR REPLAC????)

?s s13 and computer

357 S13

6669187 COMPUTER

S14 135 S13 AND COMPUTER

?s s14 not py>2000

135 S14

14465632 PY>2000

S15 123 S14 NOT PY>2000

?RD

...examined 50 records (50)

...examined 50 records (100)

...completed examining records

S16 99 RD (unique items)

```

?s monitor??? (5n) (building or facility or house or home or warehouse)
Processing
Processing
Processed 10 of 16 files ...
Processing
Completed processing all files
      2196067 MONITOR???
      4116990 BUILDING
      2289817 FACILITY
      3320319 HOUSE
      5841800 HOME
      479105 WAREHOUSE
S19 42896 MONITOR??? (5N) (BUILDING OR FACILITY OR HOUSE OR HOME OR
      WAREHOUSE)
?s replac???? (10n) (building or facility or house or home or warehouse)
Processing
Processing
Processed 10 of 16 files ...
Completed processing all files
      2624586 REPLAC????
      4116990 BUILDING
      2289817 FACILITY
      3320319 HOUSE
      5841800 HOME
      479105 WAREHOUSE
S20 104348 REPLAC???? (10N) (BUILDING OR FACILITY OR HOUSE OR HOME
      OR WAREHOUSE)
?s s19 and s20
      42896 S19
      104348 S20
S21 726 S19 AND S20
?s s21 and computer (5n) (control???? or central or center)
Processing
Processing
Processing
Processed 10 of 16 files ...
Processing
Completed processing all files
      726 S21
      6669187 COMPUTER
      6952275 CONTROL????
      4198334 CENTRAL
      3852648 CENTER
      247816 COMPUTER(5N)((CONTROL???? OR CENTRAL) OR CENTER)
S22 64 S21 AND COMPUTER (5N) (CONTROL???? OR CENTRAL OR CENTER)
?s s22 and computer (s) replac????
Processed 10 of 16 files ...
Processing
Completed processing all files
      64 S22
      6669187 COMPUTER
      2624586 REPLAC????
      90601 COMPUTER(S)REPLAC????
S23 18 S22 AND COMPUTER (S) REPLAC????
?s s23 not py>2000
      18 S23
      14465632 PY>2000
S24 16 S23 NOT PY>2000
?RD
...completed examining records
S25 12 RD (unique items)

```

18/9,K/1 (Item 1 from file: 613)  
DIALOG(R)File 613:PR Newswire  
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00935320 20030217PHM006 (THIS IS THE FULLTEXT)  
Agere Systems Announces World's Fastest Network Processor  
PR Newswire  
Monday, February 17, 2003 00:02 EST  
JOURNAL CODE: PR LANGUAGE: ENGLISH RECORD TYPE: FULLTEXT  
DOCUMENT TYPE: NEWSWIRE  
WORD COUNT: 1,535

TEXT:

ALLENTOWN, Pa., Feb. 17 /PRNewswire-FirstCall/ - Agere Systems (NYSE: AGR.A, AGR.B) today announced the world's fastest network processor that integrates four separate chips into one. This next generation network processor, called the PayloadPlus(R) APP540, makes possible revolutionary improvements in product development costs, service revenue capabilities, and

reliability of communications network equipment for the next several years.

Agere's new chip can reduce product development costs -- a critical metric in the current cash-constrained market -- by at least 50 percent compared with the nearest contending chips.

Korea-based Electronics and Telecommunications Research Institute (ETRI), one of the world's leading information technology research and development organizations, has selected Agere's new APP540 network processor as the key engine for creating Korea Telecom's country-wide next generation network. ETRI, which has a long-standing relationship with Agere, has been instrumental in enabling Korea to achieve the highest percentage of broadband connections to citizens of any country in the world.

Agere's key technical breakthrough centers on integrating four separate devices -- programmable traffic manager, multi-field classifier search engine, network processor, and Ethernet media access controller (MAC) -- into a single device capable of processing information at speeds of 5 gigabits per second (Gbits/s). Competing products require at least two chips, and in several cases three or more, to perform these four different functions at comparable speeds.

Using fewer chips decreases electronics costs, power consumption, and equipment size, as well as increases reliability.

Classification, traffic management, and network processing function like three different traffic police officers working in unison with separate yet inter-dependent functions. The classifier determines what should be done with

the voice, data, or video information entering communications equipment. The

network processor obeys processing and forwarding directions the classifier gives it. Robust traffic management helps control the flow of information exiting the network processor to most efficiently use the available bandwidth.

Robust traffic management, which supports true bandwidth and delay guarantees to 8,192 queues or more of individually scheduled streams of traffic, is important to support high-value service-level agreements for telecom carriers.

The Ethernet MAC allows direct connection to Ethernet, the world's most

popular form of data communications.

Roughly one-fourth the size of a credit card, Agere's APP540 chip is part of Agere's growing family of integrated Payload Plus network processors, traffic management and system software solutions used in communications equipment platforms. The PayloadPlus family has been selected for use in more than 70 system designs, more than half of which are top-tier manufacturers of communications equipment. In the multi-service equipment chip business, Agere possesses greater than 50 percent market share of the top-tier customers.

"Now more than ever, the communications equipment and service markets care about reducing capital and operations costs, rapidly and easily deploying new revenue generating services, and offering quick, simple, and flexible access to services over current and future networks," said Linley Gwennap, principal analyst with The Linley Group, a California-based semiconductor technology research company. "Agere's new highly integrated solution offers substantial cost reduction and programmability benefits that will maximize the value service providers can extract from their network investments."

"Agere is a leader in traffic management, having developed several generations of successful products," Gwennap added. "This technology puts the company in a strong position as traffic management becomes integrated into the network processor."

According to Ryan, Hankin, and Kent, a market research company, Agere ranks first in the world in sales of traffic management chips.\*\* "Building chips that do network processing but not traffic management is like building a road system without traffic signals such as stoplights and yield signs," said John Rolfe, marketing manager with Agere Systems.

"Recognizing the challenging environment in the communications market, Agere has been investing in advanced system chips targeted at platforms that can be consolidated and re-used across various equipment," said Mark Pinto, vice president of Agere's network processor business. "Our customers keep coming back to us with the same messages: cost reduction, multi-service revenue generation, and better reliability -- all provided in fewer and more flexible platforms. That's what this new integrated chip is all about."

Agere's PayloadPlus chips are used in various types of corporate office building and telecommunications central office equipment. Such equipment

includes multi-service provisioning platforms and switches, routers, data center switches, 2.5-generation and third-generation wireless equipment

Ethernet over Synchronous Optical Network (SONET) /Synchronous Digital Hierarchy (SDH) add/drop multiplexers, and SONET transmission systems.

"There is some ambiguity in the industry around what exactly is a network processor," said Agere's Pinto. "Some think it's a general purpose micro processor with networking interfaces and large data buffers. Some call it a look-up engine. Others a traffic manager. Still others consider it a segmentation and reassembly controller engine. Agere Systems believes network

processors require the interworking of all of the above, as embodied in the single chip APP540.

"As an alternative, the industry could define the collection of chips, that perform traffic management, classification, and other network

processor functions, required in a given system as traffic processors," Pinto added. "Because that's what the chips do, process traffic. The APP500 family offers the full range of these functions in single chips, thereby lowering development costs, power and size of equipment."

Agere's APP540 chip uses external dynamic random access memory (DRAM) chips to house classification tables and rules. Competitors use content addressable memory (CAM) or static random access memory (SRAM) chips. In a cost per information bit comparison, CAMs are more than 100 times more expensive and consume more power than DRAMs. Agere didn't just integrate four devices and memory on a single chip. Rather, it developed an architecture that takes advantage of all the various functions and inter-dependencies of those functions onto a single device.

As part of Agere's broad portfolio of network processor-based solutions, Agere also provides a high-level, application-oriented software programming environment. This environment, which can reduce both the complexity and size of software code required by a factor of 25 or more, is included in Agere's Festino(TM) comprehensive hardware and software development platform. Equipment makers using Festino can deliver their product to market several months faster and accomplish in days what would normally take them months using alternative technologies. Furthermore, the reduction in software complexity and size can save millions of dollars in software-related costs over the life of the equipment.

Agere Systems is also announcing today the PayloadPlus APP520 chip, which is essentially the same device as the APP540 yet targeted at lower-cost applications. Agere's two new chips are completely designed and ready for manufacturing now. They are scheduled to start sampling to customers in April. In quantities of 10,000, the chips are priced at \$295 and \$195 respectively.

For more product information, customers may call the Agere Systems Customer Response Center, 1-800-372-2447, Dept. B02 (in Canada, 1-800-553-2448, Dept. B02, fax number 1-610-712-4106, especially for callers outside of North America) or write to Agere Systems, Room 10A-301C, 1110 American Parkway NE, Lehigh Valley Central Campus, Allentown, Pa. 18109. Customers may also go to the following web site: <http://www.agere.com/micro/his> or email: [docmaster@agere.com](mailto:docmaster@agere.com).

Agere Systems is a premier provider of advanced integrated circuit solutions that access, move and store network information. Agere's access portfolio enables seamless network access and Internet connectivity through its industry-leading WiFi/802.11 solutions for wireless LANs and computing applications, as well as its GPRS offering for data-capable cellular phones. The company also provides custom and standard multi-service networking solutions, such as broadband Ethernet-over-SONET/SDH components and wireless infrastructure chips, to move information across metro, access and enterprise networks. Agere is the market leader in providing integrated circuits such as read-channel chips, preamplifiers and system-on-a-chip solutions for high-density storage applications. Agere's customers include the leading PC manufacturers, wireless terminal providers, network equipment suppliers and hard-disk drive providers. More information about Agere Systems is available

from its Web site at <http://www.agere.com>.

**Agere's Forward-Looking Statements**

This release contains forward-looking statements based on information available to Agere as of the date hereof. Agere's actual results could differ materially from the results stated or implied by such forward-looking statements due to a number of risks and uncertainties. These risks and uncertainties include, but are not limited to, keeping pace with technological change, dependence on new product development, price and product competition, availability of manufacturing capacity, customer demand for our products and services, general industry and market conditions, timely completion of employment reductions and other restructuring and consolidation activities, limits on our ability to issue equity to raise capital and reliance on major customers and suppliers. For a further discussion of these and other risks and uncertainties, see our annual report on Form 10-K for the fiscal year ended September 30, 2002. Agere disclaims any intention or obligation to update or revise any forward-looking statements, whether as a result of new information, future events or otherwise.

\* ETRI

\*\* Ryan, Hankin, and Kent

**SOURCE Agere Systems**

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Web site: <http://www.agere.com/micro/his>  
Web site: <http://www.agere.com>

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COMPANY NAMES: Agere Systems; KOREA TELECOM CORP; KOREA TELECOMMUNICATION AUTHORITY; LINLEY GROUP INC; LINLEY INC; LINLEY BROTHERS LTD; LINLEY LTD

INDUSTRY NAMES: CORPORATE; MARKETING; NEW PRODUCT DEVELOPMENT; COMMUNICATIONS TECHNOLOGIES; COMPANY PROFILES; COMPUTER HARDWARE; COMPUTER MEMORY; COMPUTER SOFTWARE; COMPUTERS; DATA COMMUNICATIONS; ECONOMIC DEVELOPMENT; ECONOMIC INDICATORS; ELECTRONIC COMPONENTS; ELECTRONICS INDUSTRY; INFRASTRUCTURE; INTERNET; MICROCHIPS; MOBILE COMMUNICATIONS; NETWORKS; RADIO COMMUNICATION; RESEARCH AND DEVELOPMENT; SCIENCE; SEMICONDUCTORS; TECHNOLOGY DEVELOPMENT; TELECOMMUNICATIONS; TRAFFIC; TRANSPORT

EVENT NAMES: PRODUCT LAUNCHES; NEW PRODUCT DEVELOPMENT; ADVERTISING AND PROMOTION; CORPORATE FINANCIAL DATA; MANUFACTURING AND PRODUCTION; MARKET RESEARCH; PRODUCT APPLICATIONS; PRODUCTIVITY; RESEARCH AND DEVELOPMENT; TECHNOLOGY DEVELOPMENT

**TEXT:**

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